

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants :	Matthew J. Adiletta et al.	Art Unit :	2189
Serial No. :	10/776,702	Examiner :	Behzad Peikari
Filed :	February 10, 2004	Conf. No. :	3432
Assignee :	Intel Corporation		
Title :	SRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE INCLUDING AN ADDRESS AND COMMAND QUEUE AND METHOD FOR CONTROLLING ACCESS TO A RAM		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF JANUARY 12, 2007

Please amend the above-identified application as follows: